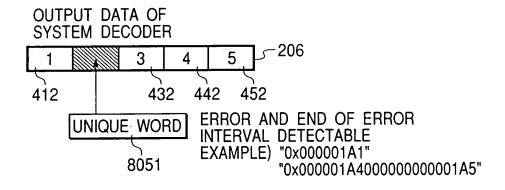
# FIG. 1



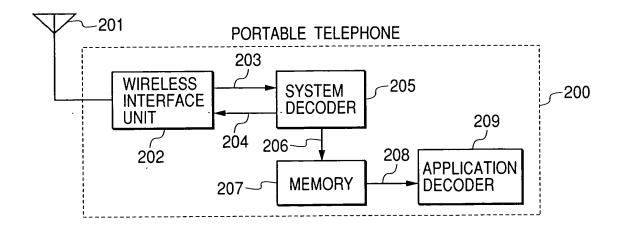


FIG. 3

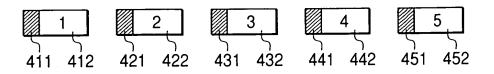


FIG. 4

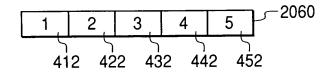


FIG. 5

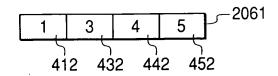


FIG. 6

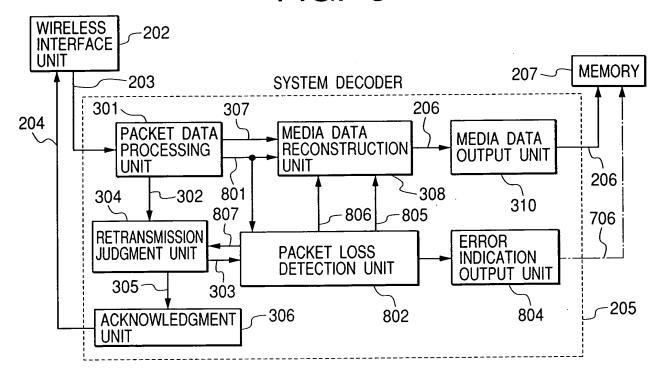
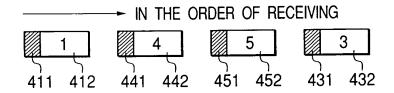


FIG. 7(a)



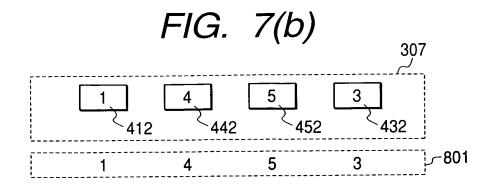
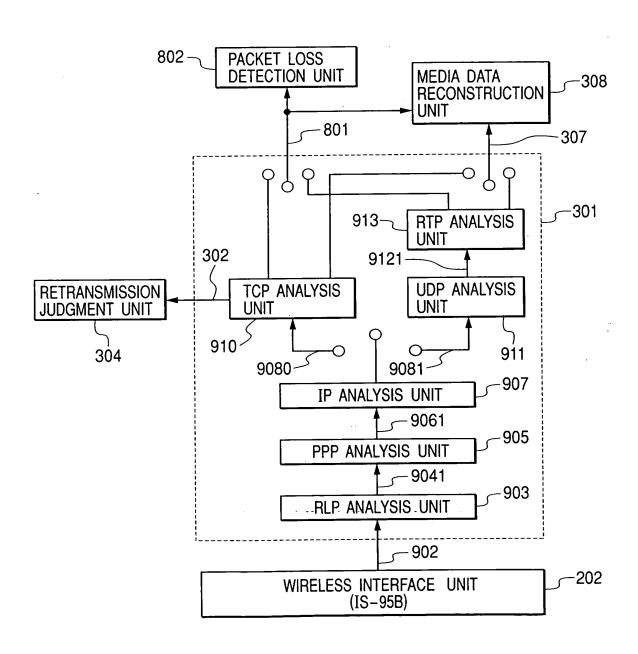


FIG. 8



## FIG. 9

#### **IPv4 PACKET**

VERSION (4 BITS)		EADER LENGTH BITS)		PRIORITY (3 BITS)		SERVIC (5 BITS	E TYPE )		AL IP LENGTH (TES)
		FRAGMENT (2 BYTES)	TIME TO LIV			PROTO (1 BYT		CHECKSUM (2 BYTES)	
SOURCE PORT DESTINATIO ADDRESS (4 BYTES) (4 BYTES)		N F	PORT	PAYLOAD (VARIABLE LENGTH)			LENGTH)		
9061	)								908

## FIG. 10

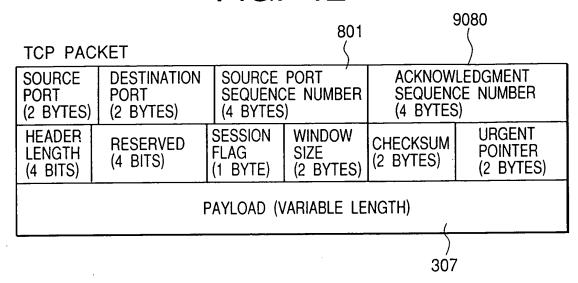
9041 PPP FRAME **DESTINATION CYCLIC** PAYLOAD (VARIABLE REDUNDANCY **PORT PROTOCOL FLAG CONTROL FLAG** CHECK (1 BYTE) **ADDRESS** (2 BYTES) (1 BYTE) (1 BYTE) LENGTH) (2 BYTES) (1 BYTE) 906

	902			
RLP FRAME				
SEQUENCE NUMBER (1 BYTE)	TYPE OF FRAME (1 BIT)	PAYLOAD LENGTH (7 BITS)	PAYLOAD (VARIABLE)	PADDING (VARIABLE)
			904	

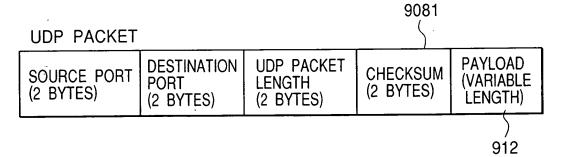
**)** c.

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## FIG. 12



## FIG. 13



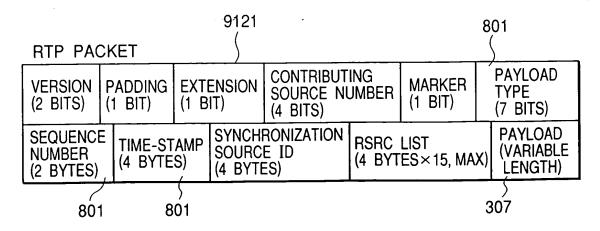
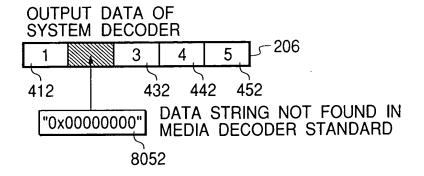


FIG. 15



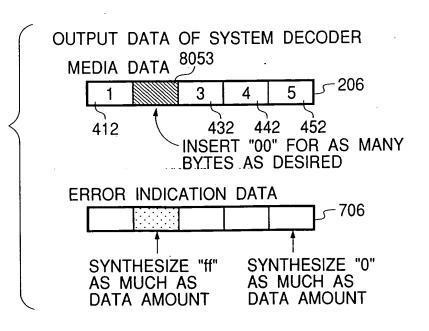


FIG. 17

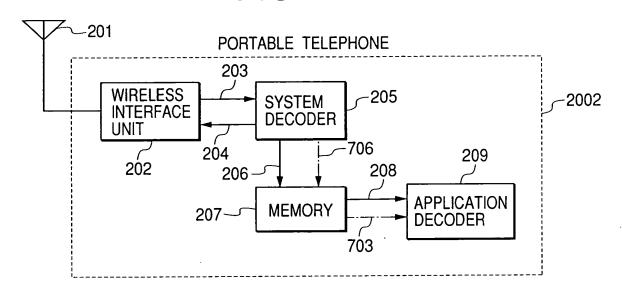
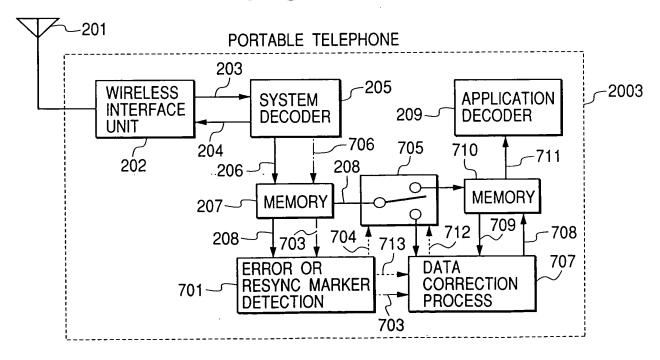


FIG. 18



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FIG. 19

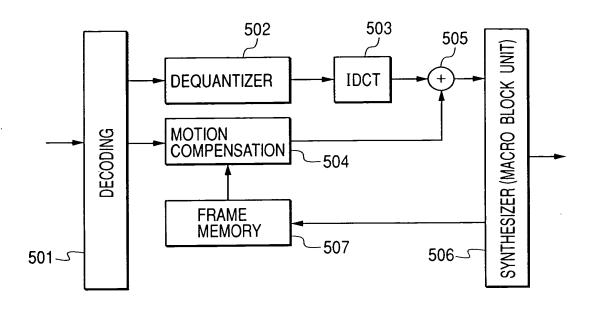
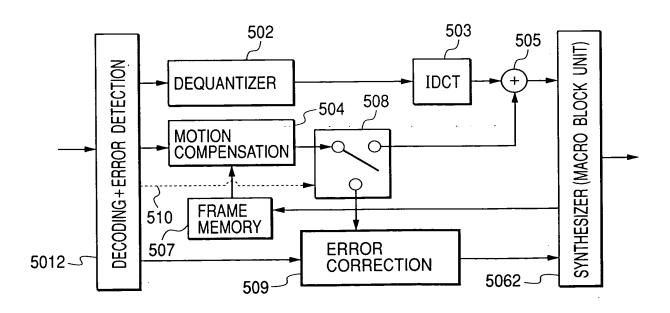
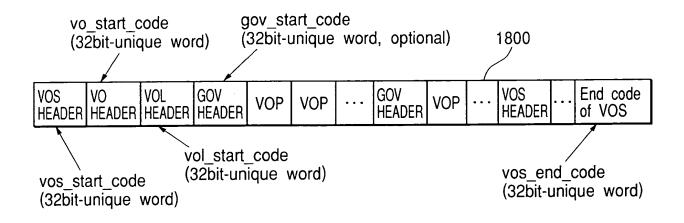


FIG. 20



## FIG. 21

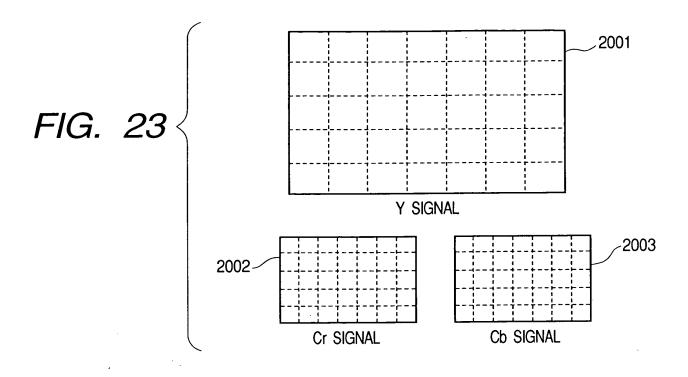


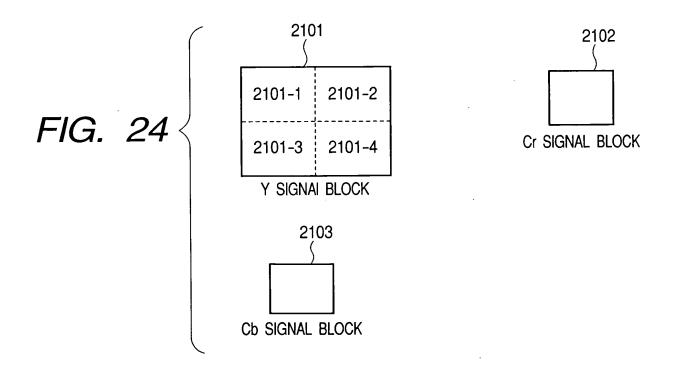
# FIG. 22

1900

modulo\_time\_base vop coding type marker bit vop start code (1bit AND ABOVE, TERMINATE WITH "0") (2bit) (1bit) (32bit-unique word) vop\_rounding\_type vop\_time\_increment marker bit -vop coded (1-16bit VARIABLE) (IN CASE OF 1bit, vop coding type !="1") (1bit) (1bit) vop fcode forward (IN CASE vop fcode backward(IN CASE intra dc vlc thr vop\_quant OF 3bit, vop coding\_type !="1") OF 3bit, vop coding type=="B") (5bit) (3bit)

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# FIG. 25

2200

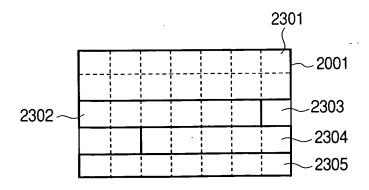
not_coded (1bit, vop_codir _type==IN CASE OF "p'	ng mcbpc (1-9bit VAF	RIABLE)	ac_pred_flag (1bit, mb_type== IN CASE OF intra or intra+q)		
cpby (1-6bit, mb_type != IN CASE OF stuffing)	dquant (2bit, m ==IN CASE ( intra+q or inte	OF	MOTION VECTOR (mb_type ==inter, inter+q or inter4v)		
DIFFERENTIAL intra DC (mb_type==intra or intra- _intra_dc_vlc==IN CASE	rq AND use		COEFFICIENT or inter DC & AC CIENT (BLOCK DESIGNATED , cbpci)		

mcbpc: mb\_type (intra, intra+q, inter, inter+q, inter4v, stuffing), cbpc not\_coded: IN CASE OF "1", mb\_type=inter, NO MOTION, mcbpc

AND THEREAFTER OMITTED

use <code>intra\_dc\_vlc</code> : <code>DETERMINE BY quant AND intra\_dc\_vlc\_thr</code>, <code>AND TAKE THE VALUE OF "0" OR "1"</code>

FIG. 26



## FIG. 27

VIDEO PACKET DATA (I-VOP)

TIDEO TAORET DATE (2 CO.)					
VIDEO PACKET HEADER	PRIORITY DATA PART (I-VOP)	dc_marker (19bit)	AC COEFFICIENT CONTROL INFORMATION (ac_pred_flag, cbpy)	AC COEFFICIENT INFORMATION	
2401	2402	2403	2404	2405	

FIG. 28

header\_extension\_code macroblock number quant scale resync marker (5bit) (1bit) (1-14bit) (17-23bit-unique word) modulo\_time\_base vop\_time\_increment marker\_bit vop\_coding\_type marker bit (1bit AND ABOVE, TERMINATE WITH "0") (1-16bit VARIABLE) (1bit) (1bit) (2bit) vop fcode backward vop fcode forward intra dc vlc thr (IN CASE OF 3bit, vop\_ (IN CASE OF 3bit, vop coding (3bit) type == "B") coding type !="1")

FIG. 29

24021

2401

#### PRIORITY DATA PART (I-VOP)

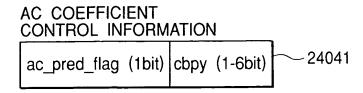
		DIFFERENTIAL intra DC COEFFICIENT
(1-9bit VARIABLE)	2bit, mb_type==intra+q)	(IN CASE OF use_intra_dc_vlc=="1")

mcbpc: mb\_type (intra, intra+q stuffing), cbpc

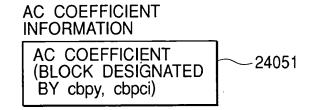
use\_intra\_dc\_vlc: DETERMINE BY quant AND intra\_dc\_vlc\_thr,

AND TAKE THE VALUE OF "0" OR "1"

## FIG. 30

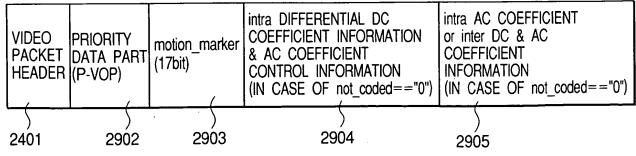


## FIG. 31



## FIG. 32

#### VIDEO PACKET DATA (P-VOP)



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#### FIG. 33

PRIORITY DATA PART (P-VOP)

not\_coded (1bit) | mcbpc | MOTION VECTOR | (1-9bit VARIABLE) | (mb\_type==inter, inter+q, or inter4v)

mcbpc: mb\_type (intra, intra+q, inter, inter+q, inter4v, stuffing), cbpc IN CASE OF not\_coded: "1", mb\_type=inter, NO MOTION, mcbpc AND THEREAFTER OMITTED

## FIG. 34

intra DIFFERENTIAL DC COEFFICIENT
INFORMATION & AC COEFFICIENT
CONTROL INFORMATION

ac\_pred\_flag (IN CASE OF 1bit, mb\_type==intra or intra+q)

DIFFERENTIAL intra DC COEFFICIENT
(mb\_type==intra or intra+q)

DIFFERENTIAL intra DC COEFFICIENT
(mb\_type==intra or intra+q)

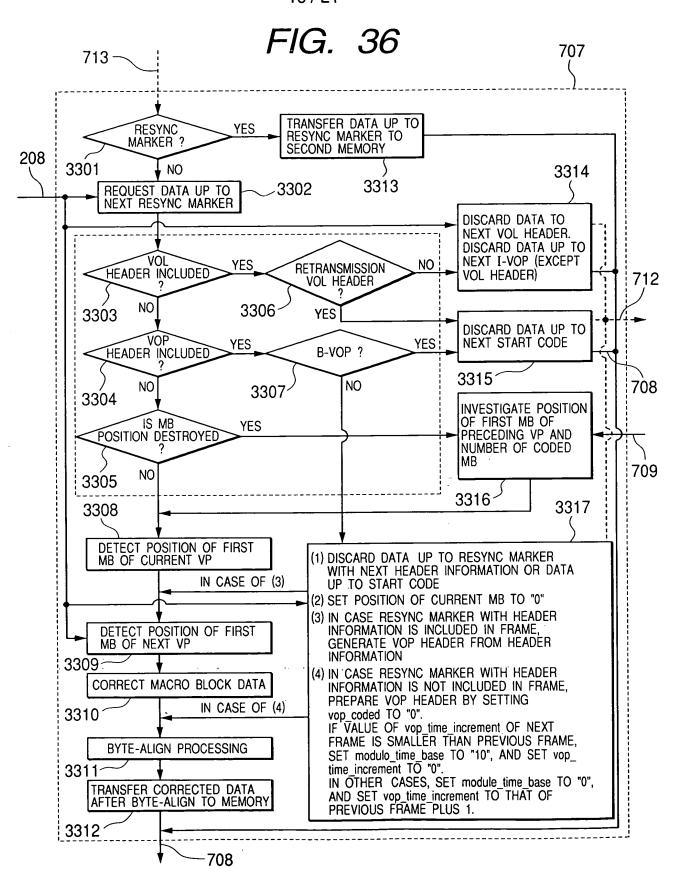
FIG. 35

intra AC COEFFICIENT or inter DC & AC COEFFICIENT INFORMATION

intra AC COEFFICIENT or inter
DC & AC COEFFICIENT
(BLOCK DESIGNATED BY cbpy, cbpc)

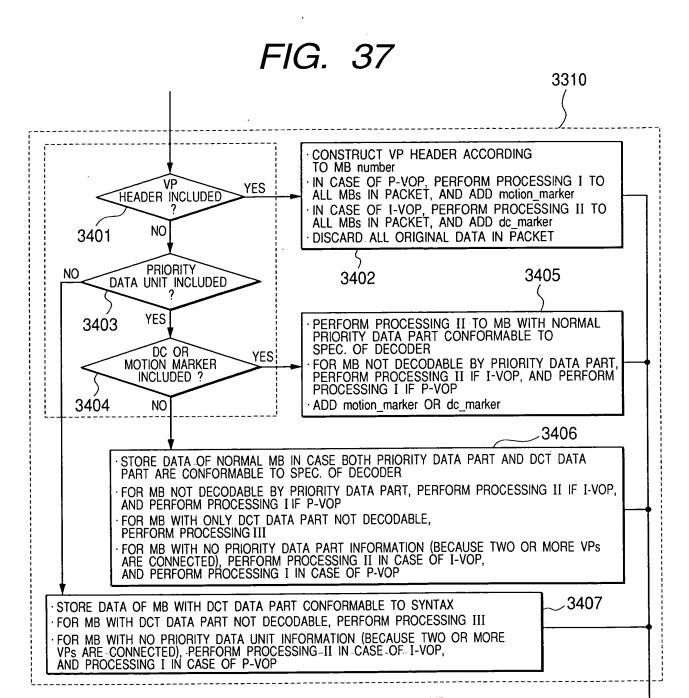
29051

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PROCESSING I: SET not coded FLAG TO "1". DELETE ORIGINAL DATA IN MB.

PROCESSING II: SET ALL DIFFERENTIAL DC COEFFICIENTS IN MB TO "0" AND SET mb\_type TO "intra" AND SET cpby AND cbpc (mcbpc) TO NO CODED BLOCK. DELETE ORIGINAL DATA IN MB.

PROCESSING III: SET cpby AND cbpc (mcbpc) TO NO CODED BLOCK.

FURTHER, IN CASE OF I-VOP, SET ac\_pred\_flag TO "0", AND DELETE AC COEFFICIENT DATA. IN CASE OF P-VOP, PERFORM PROCESSING I IF INTRA CODING. IF mb\_type

IS PREDICTIVE CODING DELETE inter DC & AC COEFFICIENT DATA.

FIG. 38

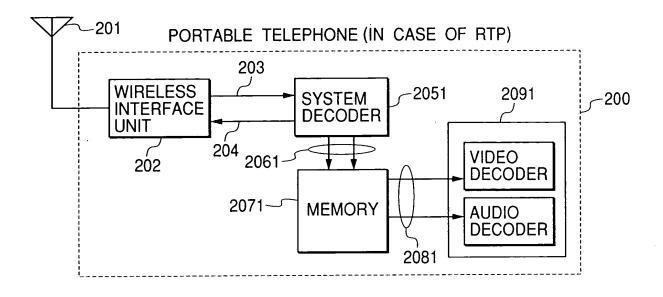
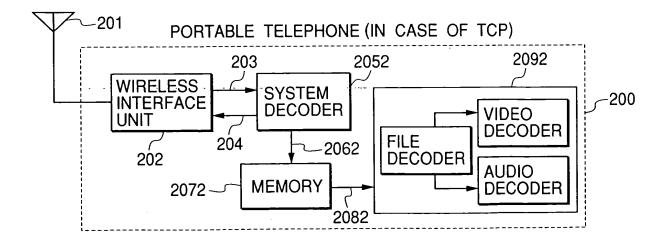


FIG. 39



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FIG. 40

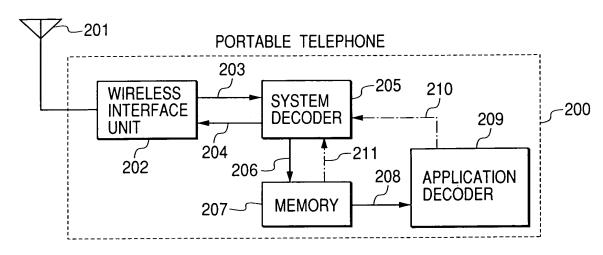


FIG. 41

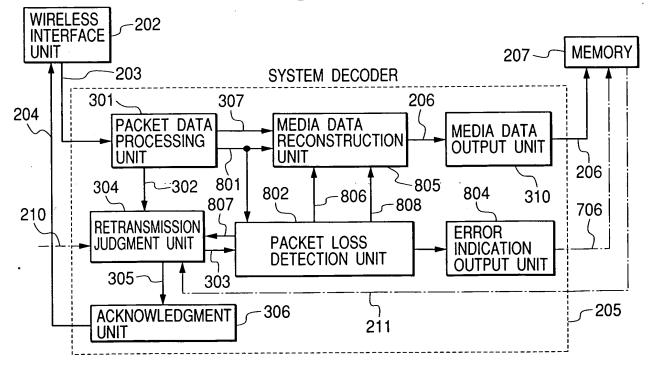


FIG. 42

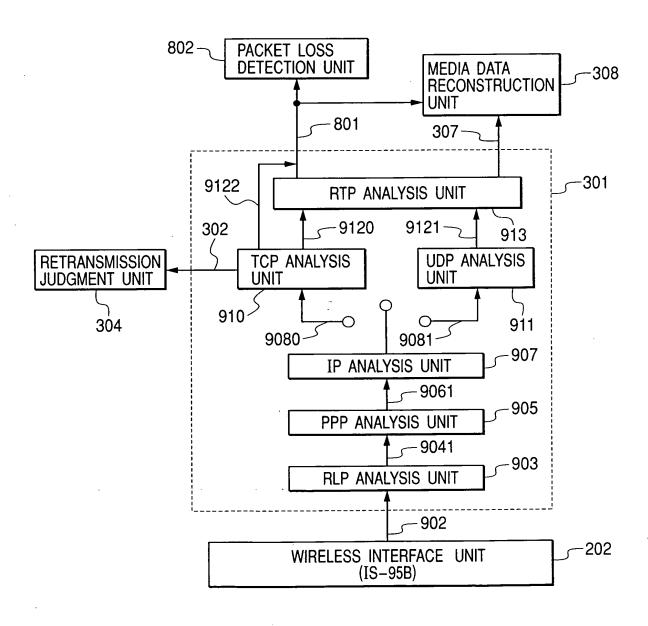


FIG. 43

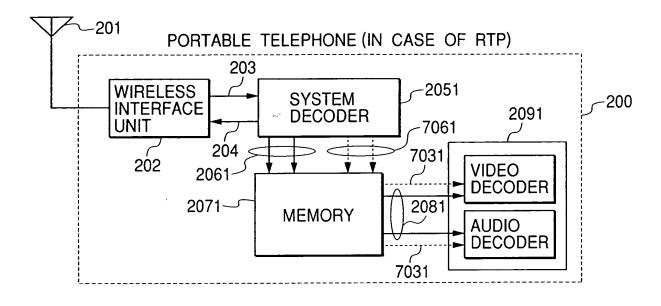


FIG. 44

